



# BTA16, BTB16 and T16 Series

SNUBBERLESS™, LOGIC LEVEL & STANDARD

16A TRIACs

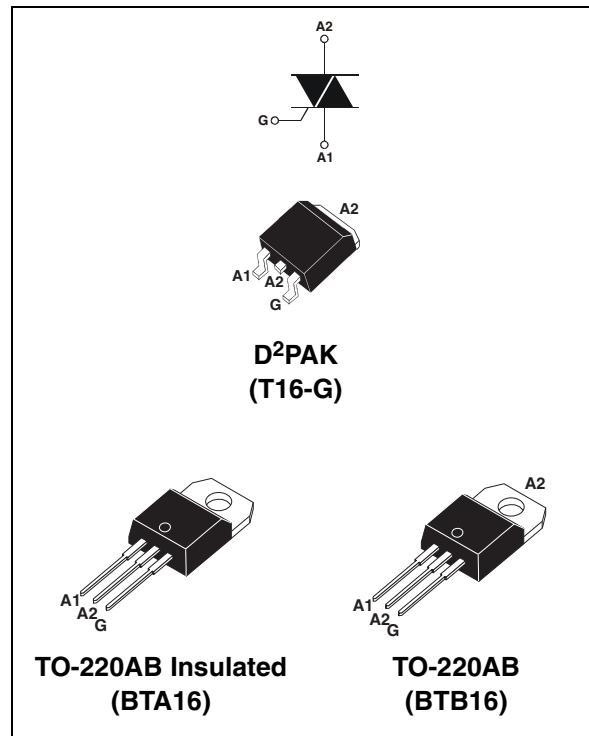
**Table 1: Main Features**

| Symbol            | Value            | Unit |
|-------------------|------------------|------|
| $I_{T(RMS)}$      | 16               | A    |
| $V_{DRM}/V_{RRM}$ | 600, 700 and 800 | V    |
| $I_{GT} (Q_1)$    | 10 to 50         | mA   |

## DESCRIPTION

Available either in through-hole or surface-mount packages, the **BTA16**, **BTB16** and **T16** triac series is suitable for general purpose AC switching. They can be used as an ON/OFF function in applications such as static relays, heating regulation, induction motor starting circuits... or for phase control operation in light dimmers, motor speed controllers, ...

The snubberless versions (BTA/BTB...W and T16 series) are specially recommended for use on inductive loads, thanks to their high commutation performances. By using an internal ceramic pad, the BTA series provides voltage insulated tab (rated at 2500V<sub>RMS</sub>) complying with UL standards (File ref.: E81734).



**Table 2: Order Codes**

| Part Number   | Marking                    |
|---------------|----------------------------|
| BTA16-xxxxxRG | See page table 8 on page 8 |
| BTB16-xxxxxRG |                            |
| T16xx-xxxG    |                            |

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**Table 3: Absolute Maximum Ratings**

| Symbol             | Parameter   |                               |                     | Value                          | Unit             |
|--------------------|---|-------------------------------|---------------------|--------------------------------|------------------|
| $I_{T(RMS)}$       | RMS on-state current (full sine wave)   | D <sup>2</sup> PAK / TO-220AB | $T_c = 100^\circ C$ | 16                             | A                |
|                    |   | TO-220AB Ins.                 | $T_c = 15^\circ C$  |                                |                  |
| $I_{TSM}$          | Non repetitive surge peak on-state current (full cycle, $T_j$ initial = 25°C)         | $F = 50$ Hz                   | $t = 20$ ms         | 160                            | A                |
|                    |   | $F = 60$ Hz                   | $t = 16.7$ ms       | 168                            |                  |
| $I^2t$             | $I^2t$ Value for fusing   | $t_p = 10$ ms                 |                     | 144                            | A <sup>2</sup> s |
| $dI/dt$            | Critical rate of rise of on-state current $I_G = 2 \times I_{GT}$ , $t_r \leq 100$ ns | $F = 120$ Hz                  | $T_j = 125^\circ C$ | 50                             | A/μs             |
| $V_{DSM}/V_{RSM}$  | Non repetitive surge peak off-state voltage   | $t_p = 10$ ms                 | $T_j = 25^\circ C$  | $V_{DSM}/V_{RSM} + 100$        | V                |
| $I_{GM}$           | Peak gate current   | $t_p = 20$ μs                 | $T_j = 125^\circ C$ | 4                              | A                |
| $P_{G(AV)}$        | Average gate power dissipation  |                               | $T_j = 125^\circ C$ | 1                              | W                |
| $T_{stg}$<br>$T_j$ | Storage junction temperature range<br>Operating junction temperature range            |                               |                     | - 40 to + 150<br>- 40 to + 125 | °C               |

**Tables 4: Electrical Characteristics ( $T_j = 25^\circ C$ , unless otherwise specified)**

■ SNUBBERLESS and Logic Level (3 quadrants)

| Symbol       | Test Conditions                                       | Quadrant            |      | T16   |     |     |      | BTA16 / BTB16 |  |  |  | Unit |
|--------------|---|---------------------|------|-------|-----|-----|------|---------------|--|--|--|------|
|              |   |                     |      | T1635 | SW  | CW  | BW   |               |  |  |  |      |
| $I_{GT}$ (1) | $V_D = 12$ V $R_L = 33$ Ω                             | I - II - III        | MAX. | 35    | 10  | 35  | 50   |               |  |  |  | mA   |
| $V_{GT}$     |   | I - II - III        | MAX. |       |     |     |      | 1.3           |  |  |  | V    |
| $V_{GD}$     | $V_D = V_{DRM}$ $R_L = 3.3$ kΩ<br>$T_j = 125^\circ C$ | I - II - III        | MIN. |       |     |     |      | 0.2           |  |  |  | V    |
| $I_H$ (2)    | $I_T = 500$ mA  |                     | MAX. | 35    | 15  | 35  | 50   |               |  |  |  | mA   |
| $I_L$        | $I_G = 1.2 I_{GT}$                                    | I - III             | MAX. | 50    | 25  | 50  | 70   |               |  |  |  | mA   |
|              |   | II                  |      | 60    | 30  | 60  | 80   |               |  |  |  |      |
| $dV/dt$ (2)  | $V_D = 67\% V_{DRM}$ gate open                        | $T_j = 125^\circ C$ | MIN. | 500   | 40  | 500 | 1000 |               |  |  |  | V/μs |
| (dI/dt)c (2) | (dV/dt)c = 0.1 V/μs                                   | $T_j = 125^\circ C$ | MIN. | -     | 8.5 | -   | -    |               |  |  |  | A/ms |
|              | (dV/dt)c = 10 V/μs                                    | $T_j = 125^\circ C$ |      | -     | 3.0 | -   | -    |               |  |  |  |      |
|              | Without snubber                                       | $T_j = 125^\circ C$ |      | 8.5   | -   | 8.5 | 14   |               |  |  |  |      |

■ Standard (4 quadrants)

| Symbol              | Test Conditions   | Quadrant               |      | BTA16 / BTB16 |           | Unit |
|---------------------|---|------------------------|------|---------------|-----------|------|
|                     |   |                        |      | C             | B         |      |
| I <sub>GT</sub> (1) | V <sub>D</sub> = 12 V    R <sub>L</sub> = 33 Ω                                      | I - II - III<br>IV     | MAX. | 25<br>50      | 50<br>100 | mA   |
| V <sub>GT</sub>     |   | ALL                    | MAX. | 1.3           |           | V    |
| V <sub>GD</sub>     | V <sub>D</sub> = V <sub>DRM</sub> R <sub>L</sub> = 3.3 kΩ    T <sub>j</sub> = 125°C | ALL                    | MIN. | 0.2           |           | V    |
| I <sub>H</sub> (2)  | I <sub>T</sub> = 500 mA   |                        | MAX. | 25            | 50        | mA   |
| I <sub>L</sub>      | I <sub>G</sub> = 1.2 I <sub>GT</sub>  | I - III - IV           | MAX. | 40            | 60        | mA   |
|                     |   | II                     |      | 80            | 120       |      |
| dV/dt (2)           | V <sub>D</sub> = 67 %V <sub>DRM</sub> gate open                                     | T <sub>j</sub> = 125°C | MIN. | 200           | 400       | V/μs |
| (dV/dt)c (2)        | (dI/dt)c = 7 A/ms   | T <sub>j</sub> = 125°C | MIN. | 5             | 10        | V/μs |

Table 5: Static Characteristics

| Symbol                               | Test Conditions                                     |                        |      | Value | Unit |
|--------------------------------------|---|------------------------|------|-------|------|
| V <sub>T</sub> (2)                   | I <sub>TM</sub> = 22.5 A    t <sub>p</sub> = 380 μs | T <sub>j</sub> = 25°C  | MAX. | 1.55  | V    |
| V <sub>to</sub> (2)                  | Threshold voltage                                   | T <sub>j</sub> = 125°C | MAX. | 0.85  | V    |
| R <sub>d</sub> (2)                   | Dynamic resistance                                  | T <sub>j</sub> = 125°C | MAX. | 25    | mΩ   |
| I <sub>DRM</sub><br>I <sub>RRM</sub> | V <sub>DRM</sub> = V <sub>RRM</sub>                 | T <sub>j</sub> = 25°C  | MAX. | 5     | μA   |
|                                      |   | T <sub>j</sub> = 125°C |      | 2     | mA   |

Note 1: minimum I<sub>GT</sub> is guaranteed at 5% of I<sub>GT</sub> max.

Note 2: for both polarities of A2 referenced to A1.

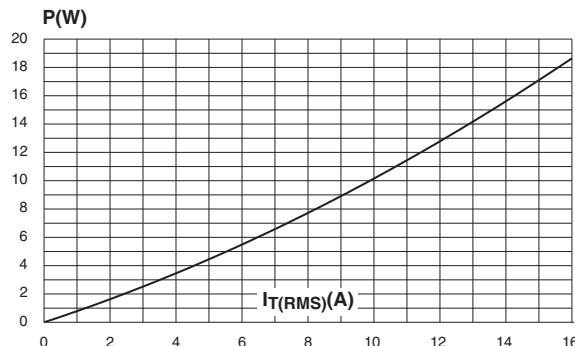
Table 6: Thermal resistance

| Symbol               | Parameter             |                               |                               | Value | Unit |
|----------------------|-----------------------|-------------------------------|-------------------------------|-------|------|
| R <sub>th(j-c)</sub> | Junction to case (AC) | D <sup>2</sup> PAK / TO-220AB |                               | 1.2   | °C/W |
|                      |                       | TO-220AB Insulated            |                               | 2.1   |      |
| R <sub>th(j-a)</sub> | Junction to ambient   | S = 1 cm <sup>2</sup>         | D <sup>2</sup> PAK            | 45    | °C/W |
|                      |                       |                               | TO-220AB / TO-220AB Insulated | 60    |      |

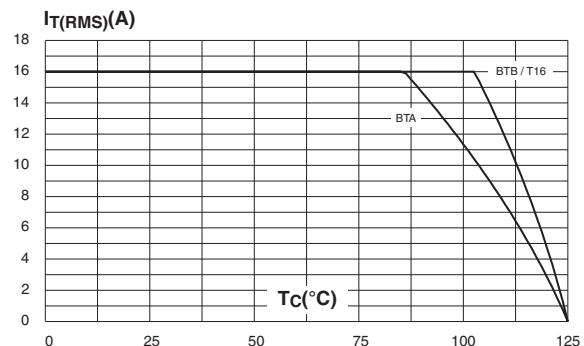
S = Copper surface under tab.

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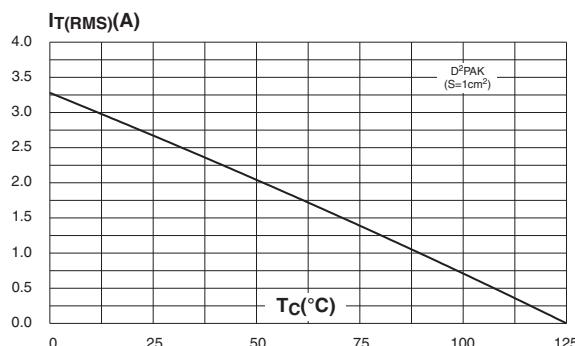
**Figure 1: Maximum power dissipation versus RMS on-state current (full cycle)**



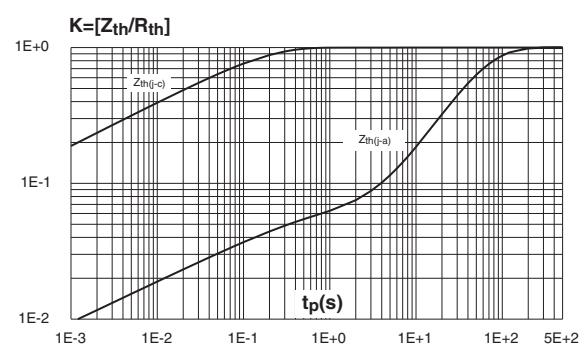
**Figure 2: RMS on-state current versus case temperature (full cycle)**



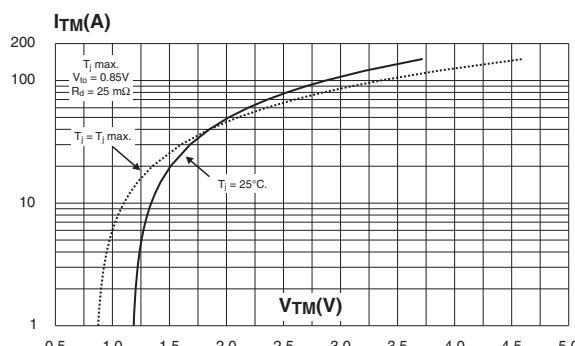
**Figure 3: RMS on-state current versus ambient temperature (printed circuit board FR4, copper thickness: 35µm) (full cycle)**



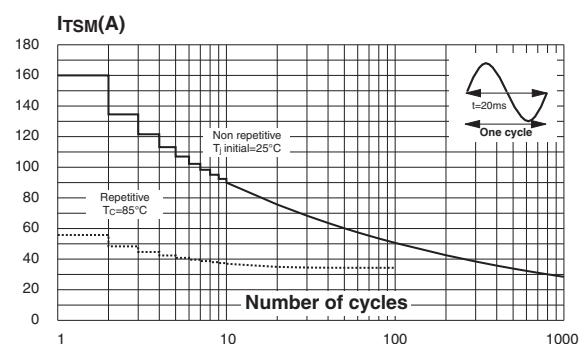
**Figure 4: Relative variation of thermal impedance versus pulse duration**



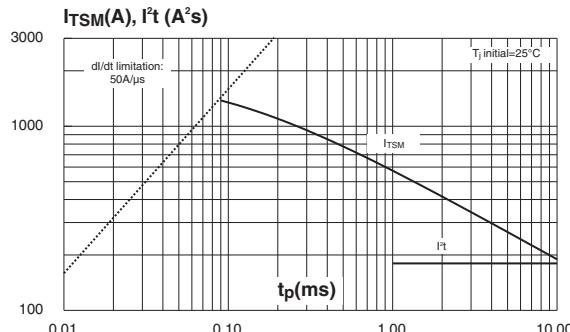
**Figure 5: On-state characteristics (maximum values)**



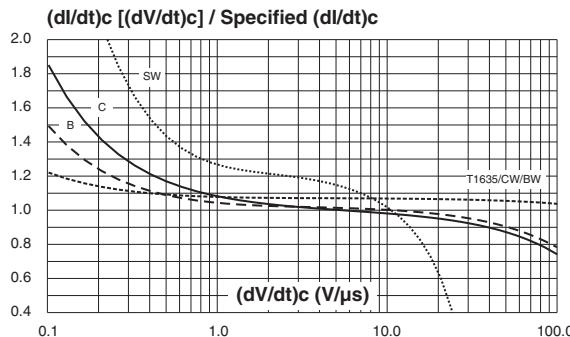
**Figure 6: Surge peak on-state current versus number of cycles**



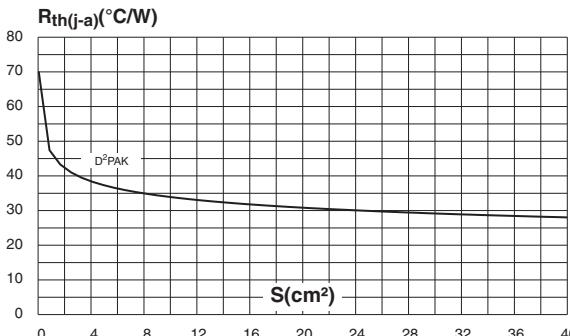
**Figure 7: Non-repetitive surge peak on-state current for a sinusoidal pulse with width  $t_p < 10$  ms and corresponding value of  $I^2t$**



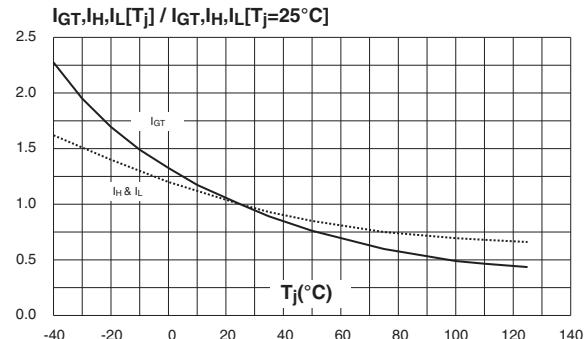
**Figure 9: Relative variation of critical rate of decrease of main current versus  $(dV/dt)c$  (typical values) (Snubberless & Logic level types)**



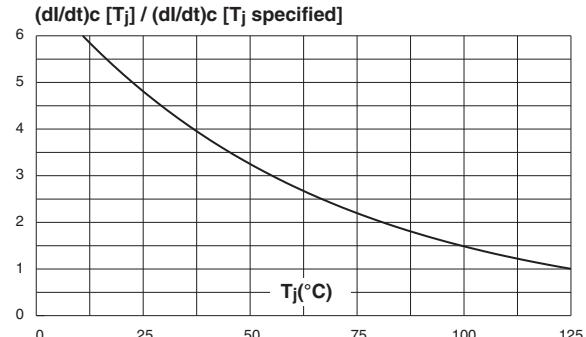
**Figure 11: D<sup>2</sup>PAK Thermal resistance junction to ambient versus copper surface under tab (printed circuit board FR4, copper thickness: 35  $\mu m$ )**



**Figure 8: Relative variation of gate trigger current, holding current and latching current versus junction temperature (typical values)**



**Figure 10: Relative variation of critical rate of decrease of main current versus  $(dV/dt)c$  (typical values) (Standard types)**



## BTA16, BTB16 and T16 Series

Figure 12: Ordering Information Scheme (BTA16 and BTB16 series)

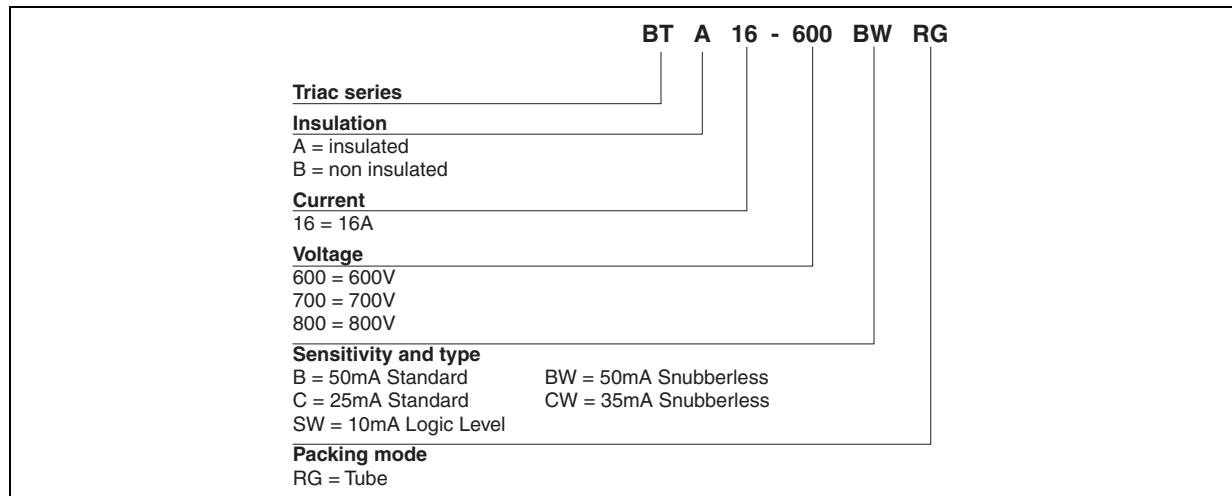


Figure 13: Ordering Information Scheme (T16 series)

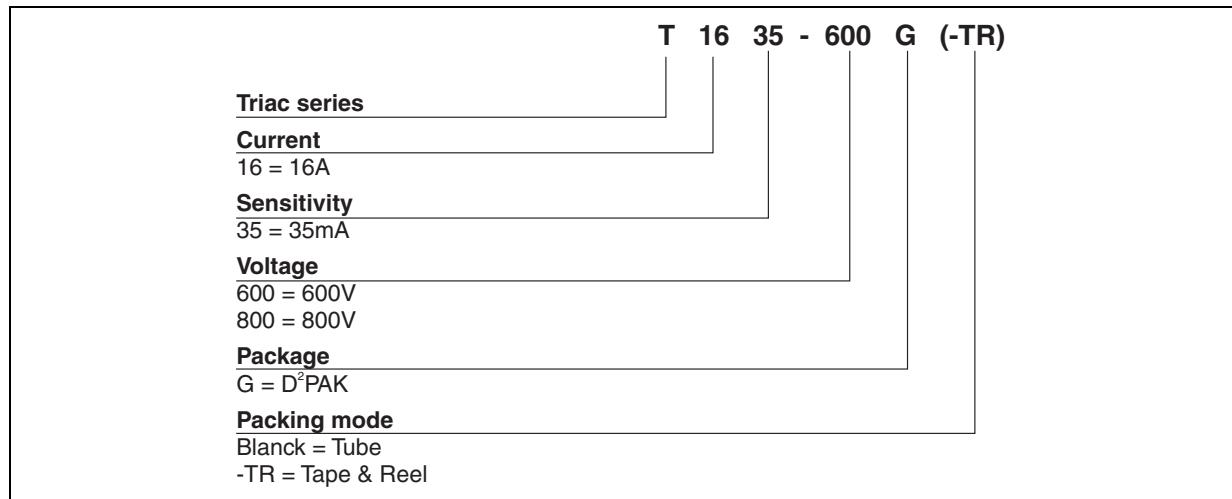


Table 7: Product Selector

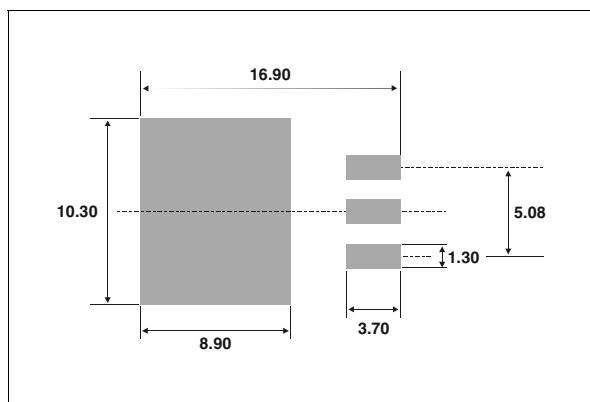
| Part Numbers    | Voltage (xxx) |       |       | Sensitivity | Type        | Package            |
|-----------------|---------------|-------|-------|-------------|-------------|--------------------|
|                 | 600 V         | 700 V | 800 V |             |             |                    |
| BTA/BTB16-xxxB  | X             | X     | X     | 50 mA       | Standard    | TO-220AB           |
| BTA/BTB16-xxxBW | X             | X     | X     | 50 mA       | Snubberless | TO-220AB           |
| BTA/BTB16-xxxC  | X             | X     | X     | 25 mA       | Standard    | TO-220AB           |
| BTA/BTB16-xxxCW | X             | X     | X     | 35 mA       | Snubberless | TO-220AB           |
| BTA/BTB16-xxxFW | X             | X     | X     | 10 mA       | Logic level | TO-220AB           |
| T1635-xxxF      | X             |       | X     | 35 mA       | Snubberless | D <sup>2</sup> PAK |

**BTB:** non insulated TO-220AB package

Figure 14: D<sup>2</sup>PAK Package Mechanical Data

| REF. | DIMENSIONS  |      |       |        |       |       |
|------|-------------|------|-------|--------|-------|-------|
|      | Millimeters |      |       | Inches |       |       |
|      | Min.        | Typ. | Max.  | Min.   | Typ.  | Max.  |
| A    | 4.30        |      | 4.60  | 0.169  |       | 0.181 |
| A1   | 2.49        |      | 2.69  | 0.098  |       | 0.106 |
| A2   | 0.03        |      | 0.23  | 0.001  |       | 0.009 |
| B    | 0.70        |      | 0.93  | 0.027  |       | 0.037 |
| B2   | 1.25        | 1.40 |       | 0.048  | 0.055 |       |
| C    | 0.45        |      | 0.60  | 0.017  |       | 0.024 |
| C2   | 1.21        |      | 1.36  | 0.047  |       | 0.054 |
| D    | 8.95        |      | 9.35  | 0.352  |       | 0.368 |
| E    | 10.00       |      | 10.28 | 0.393  |       | 0.405 |
| G    | 4.88        |      | 5.28  | 0.192  |       | 0.208 |
| L    | 15.00       |      | 15.85 | 0.590  |       | 0.624 |
| L2   | 1.27        |      | 1.40  | 0.050  |       | 0.055 |
| L3   | 1.40        |      | 1.75  | 0.055  |       | 0.069 |
| R    |             | 0.40 |       |        | 0.016 |       |
| V2   | 0°          |      | 8°    | 0°     |       | 8°    |

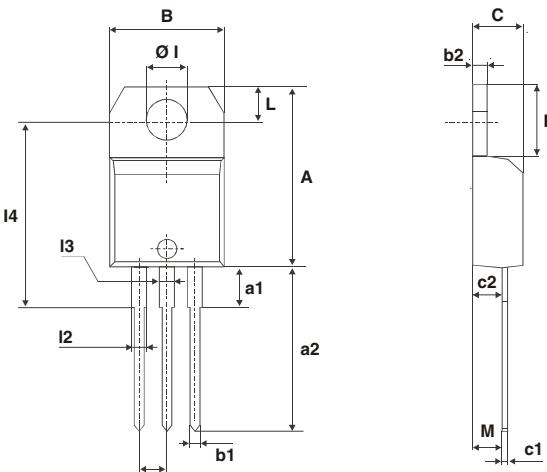
Figure 15: D<sup>2</sup>PAK Foot Print Dimensions  
(in millimeters)



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Figure 16: TO-220AB Package Mechanical Data

| REF. | DIMENSIONS  |       |       |        |       |       |
|------|-------------|-------|-------|--------|-------|-------|
|      | Millimeters |       |       | Inches |       |       |
|      | Min.        | Typ.  | Max.  | Min.   | Typ.  | Max.  |
| A    | 15.20       |       | 15.90 | 0.598  |       | 0.625 |
| a1   |             | 3.75  |       |        | 0.147 |       |
| a2   | 13.00       |       | 14.00 | 0.511  |       | 0.551 |
| B    | 10.00       |       | 10.40 | 0.393  |       | 0.409 |
| b1   | 0.61        |       | 0.88  | 0.024  |       | 0.034 |
| b2   | 1.23        |       | 1.32  | 0.048  |       | 0.051 |
| C    | 4.40        |       | 4.60  | 0.173  |       | 0.181 |
| c1   | 0.49        |       | 0.70  | 0.019  |       | 0.027 |
| c2   | 2.40        |       | 2.72  | 0.094  |       | 0.107 |
| e    | 2.40        |       | 2.70  | 0.094  |       | 0.106 |
| F    | 6.20        |       | 6.60  | 0.244  |       | 0.259 |
| ØI   | 3.75        |       | 3.85  | 0.147  |       | 0.151 |
| I4   | 15.80       | 16.40 | 16.80 | 0.622  | 0.646 | 0.661 |
| L    | 2.65        |       | 2.95  | 0.104  |       | 0.116 |
| I2   | 1.14        |       | 1.70  | 0.044  |       | 0.066 |
| I3   | 1.14        |       | 1.70  | 0.044  |       | 0.066 |
| M    |             | 2.60  |       |        | 0.102 |       |



In order to meet environmental requirements, ST offers these devices in ECOPACK® packages. These packages have a Lead-free second level interconnect . The category of second level interconnect is marked on the package and on the inner box label, in compliance with JEDEC Standard JESD97. The maximum ratings related to soldering conditions are also marked on the inner box label. ECOPACK is an ST trademark. ECOPACK specifications are available at: [www.st.com](http://www.st.com).

Table 8: Ordering Information

| Ordering type     | Marking        | Package            | Weight | Base qty | Delivery mode |
|-------------------|----------------|--------------------|--------|----------|---------------|
| BTA/BTB16-xxxyzRG | BTA/BTB16xxxyz | TO-220AB           | 2.3 g  | 50       | Tube          |
| T1635-xxG         | T1635xxG       | D <sup>2</sup> PAK | 1.5 g  | 50       | Tube          |
| T1635-xxG-TR      | T1635xxG       |                    |        | 1000     | Tape & reel   |

Note: xx = voltage, yy = sensitivity, z = type

Table 9: Revision History

| Date        | Revision | Description of Changes  |
|-------------|----------|---|
| Oct-2002    | 6A       | Last update.  |
| 13-Feb-2006 | 7        | TO-220AB delivery mode changed from bulk to tube.<br>ECOPACK statement added. |

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